

Appln. No. 10/724,347
Amdt. dated October 25, 2005
Reply to Notice of Allowance of September 23, 2005

OK
To Engr
8/18
11/2/05
This listing of claims will replace all prior versions, and listings, of claims in the application:

Listing of Claims:

Claims 1-14 (canceled)

Claim 15 (previously presented): A method for suppressing interference in an electrical signal, the method comprising the steps of:

sampling the electrical signal at a first sampling frequency to obtain a first sequence of samples;

5 identifying some of the samples in the first sequence of samples on the basis of predetermined criteria; and

downsampling at least the identified samples of said first sequence to obtain a second sequence of samples from the identified samples of said first sequence, the second sequence of samples forming a digital presentation of the electrical signal in which

10 the interference is suppressed.

Claim 16 (previously presented): A method according to claim 15 further comprising the step of generating information about the interference content in the samples and wherein the identification step is further defined as identifying samples in the first sequence of samples on the basis of their interference content.

Claim 17 (previously presented): A method according to claim 16 wherein the generating step is further defined as attaching a flag to each of the samples to indicate whether the sample is free of interference or not.

Claim 18 (previously presented): The method according to claim 16 wherein the identifying step is further defined as identifying samples that are artifact free.